

S/N 09/691,004

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Johannes P. Mondt

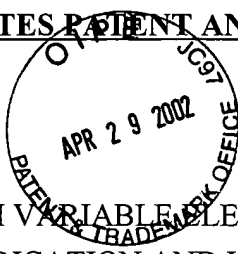
Serial No.: 09/691,004

Group Art Unit: 2826

Filed: October 18, 2000

Docket: 303.324US4

Title: TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND  
METHODS OF FABRICATION AND USE



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RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents

Washington, D.C. 20231

In response to the Restriction Requirement dated 26 March 2002, the applicant elects, without traverse, the claims of Group I, including claims 36-45, 56-67, 68-85, and 98-100. Please cancel the remaining claims of Groups II and III, including claims 46-51, 52-55, and 86-97 without prejudice or disclaimer. The applicant reserves the right to reintroduce claims 46-51, 52-55, and 86-97 in divisional applications at a later date.


The Examiner is invited to contact the applicant's representatives at the below-listed telephone number if there are any questions regarding this Response or if prosecution of this application may be assisted thereby.

Respectfully submitted,

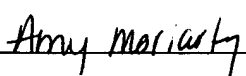
LEONARD FORBES ET AL.

By their Representatives,  
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
P.O. Box 2938  
Minneapolis, MN 55402  
(612) 373-6973

Date 24 April 2002

By   
Robert E. Mates  
Reg. No. 35,271

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this 24th day of April, 2002.

Name 

Signature 